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07/23/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10064518	07/23/2002	365	13502	2818	H.C. H.C.B.

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**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	MXIP0088USA
Verified and Acknowledged Examiners's initials		
TITLE : Method for programming, reading, and erasing a non-volatile memory with multi-level output currents		

U.S. DEPT. OF COMMERCE PAT. & TM. PTO 4761 Rev. 12/04

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Total Claims		Print Claim for O.G.
Assistant Examiner				
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
		Application Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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